DAC－TALENT

Engineering features

●Sign-Magnitude 20-bit D/A processor without Zero Cross Distortion.
●Glitchless D/A Converter eliminates sonic degradation associated with de-glitch circuitry.
●3-Sampling Frequencies(32kHz,44.1kHz,& 48kHz) with Automatic Adaptation.
●Optical (Tos-link) + Coaxial Digital (75Ω)Inputs.
●Optical Isolation between Digital and DAC Analog Section.
●20-bit, 8-times Over Sampling Digital Filter.
●Absolute Phase Selection, and De-Emphasis in Digital Domain.
●De-Emphasis Indicator.
●Muting Switch.
●Jitter Free Clock Generation accomplished by Double PLL(Phase Locked Loop).
●1(Single) Pole Low Pass Filter(-6dB/octave).
●Compact Short-Cut Located Circuits.
●Low Power Consumption.
●Low Digital Signal Interference for Internal and External Fields.

SPECIFICATIONS

Type●3 Sampling Frequency Adaptive D/A Processor
Format●EIA Standard
Quantum ●16-bit Linear
Sampling Frequency●32kHz ±0.1, 44.1kHz ±0.1%, 48kHz ±0.1%
D/A Conversion●Fixed 20-bit
Digital Filter●20-bit, 8-times Over Sampling
De-Emphasis ●Digital Domain
Total Harmonic Distortion + Noise ●0.0015%(1kHz, 0dB)
0.015%(1kHz,-20dB)
1.5% (1kHz,-60dB)
Signal to Noise Ratio ●118dB
Dynamic Range ●104dB
Channel Separation●120dB
Digital Input ●Optical(×1) -14.5～24dBm
Co-axial (×1) 0.5Vp-p, 75Ω
Analog Output Level ●1.9V
Analog Output Impedance ● 220Ω
Power Consumption ●6W
Dimention and Weight●130(W)× 37(H)×350(D)mm, 1.5kg